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Sheet	1	of	1	<b>Application Number</b>	Not assigned yet
				<b>Filing Date</b>	On even date herewith
				<b>First Named Inventor</b>	K. YAMASHITA, et al.
				<b>Art Unit</b>	Not assigned yet
				<b>Examiner Name</b>	Not assigned yet
				<b>Attorney Docket Number</b>	501.41072CX1

U.S. PATENT DOCUMENTS					
Examiner Initials'	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number-Kind Code <sup>2</sup> (if known)			
NV		5,724,004		03-1998	Reif et al.
NV		5,990,748		11-1999	Samuels, John
NV		6,052,032		04-2000	Jarvinen, Esko
NV		6,215,358		04-2001	Hon et al.
NV		6,359,948		03-2002	Tundic et al.
NV		6,372,937		10-2002	Gerard et al.

OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published		T <sup>2</sup>
NV		"Thermally stable power HBT by the bias circuit using Schottky diode" Keiichi Murayama et al., Semiconductor Device Research Center, Matsushita Electronics Corporation, pp. 1-4 and p.65		

Examiner Signature	/Nguyen Vo/	Date Considered	05/21/2006
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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